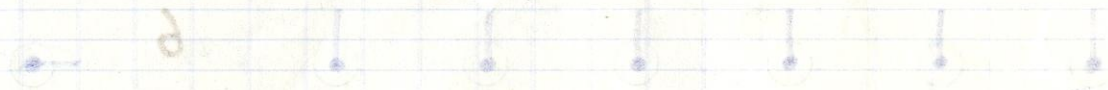


FRONT LAY OUT

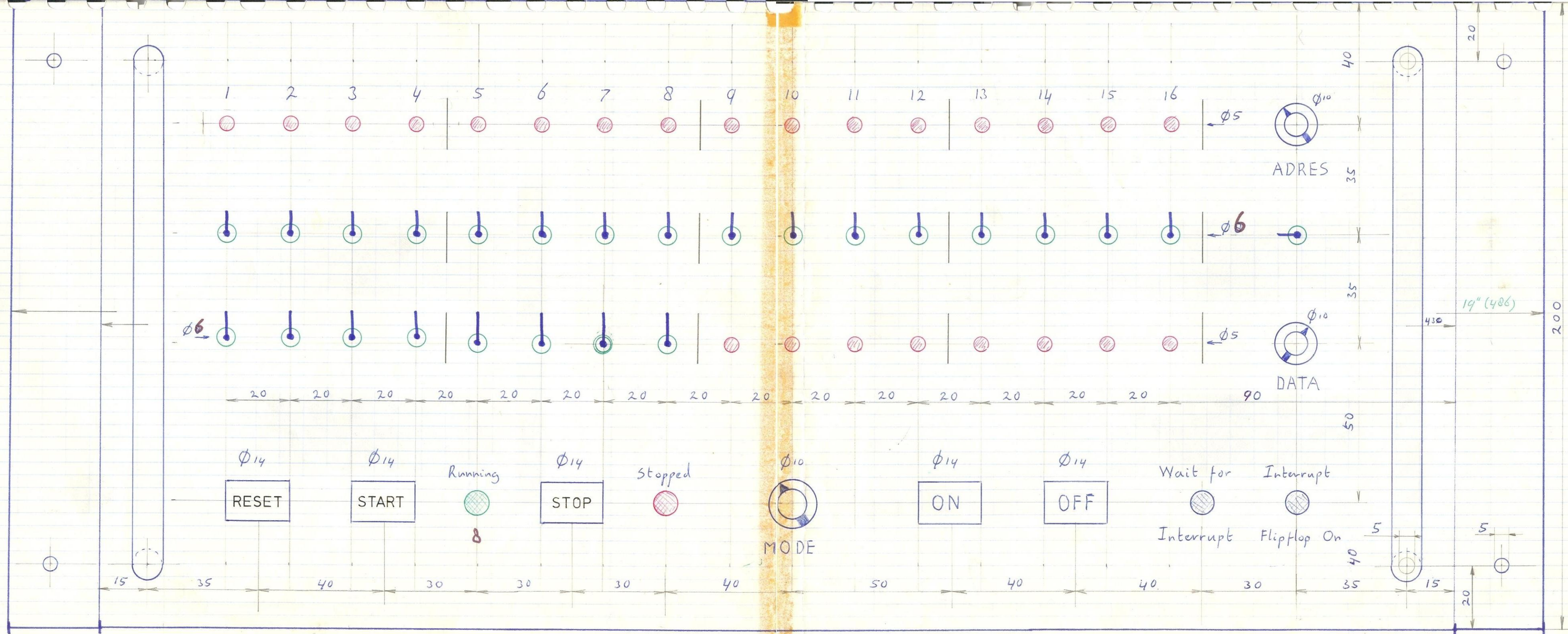
FROM THE CPU

FRONT LAY OUT

FROM THE CPU



H3S. 29/2/76



A B C D E F G H I J K L M N O P

1

0-1	2-3	4-5	6-7	0-1	2-3	4-5	6-7	0-1	2-3	4-5	6-7				
M selector				L selector				8 Led selector				Carry	Shift	-	-
74153				74153				74153				7474	7474	7408	7414

1

2

	Op. (L)	(H)	(L)	(H)											
Star.	I register		PC		Acc	R=0	-	Stat.	Exor	Comp	Adder	AND	Rreg		
7442	7475		74191		7475	7430	7402	7475	7486	7485	7483	7408	7475		

2

3

	Op. (L)	(H)	(L)	(H)											
Inv	I register		PC		Acc	A=0	Oscl	Stat.	Exor	Comp	Adder	AND	Rreg		
7414	7475		74191		7475	7430	74157	7475	7486	7485	7483	7408	7475		

3

4

0-1	2-3	4-5	6-7	8-9	10-11	12-13	14-15	0-3	4-7	8-11	12-15	0-3	4-7	8-11	12-15
K selector								M register				16 Led selector			
74153								74191				74157			

4

H3522

A board lay out

22/7/79

A B C D E F G H I J K L M N O P

5	1-16	Pcc	-	FF Driver	-	R/C's	-	-	Conn naar frontpaneel	0-3 4-7	8 Led buf.	5
	74154	7486	7476	7437	7476	7402	7408	functie	16Sw	8L+8S	16L	

6	0	1	2	3	Timing	-	-	-	-	-	0-3 4-7 8-11 12-15	16 Led Buffer	6
	N selector					74151	7408	7400	7400	7402	7404		

7	4	5	6	7	Timing	16-23	24-31	32-39	0-3	0-1	2-3	0-3	Prog Osc. Peter Power	7
	N selector					74151	7408	MM5203Q	74157	74153	74175	7404		

8	Testpat selector				Timing	8-15	resv	0-7	4-7	4-5	6-7	4-7	Conn	8
						74151	74153	7408	MM5203Q	74157	74153	74175		

HSS 22

Aboard layout.

22/7/79